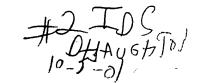
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Devins et al.

Atty Docket: BUR920000154US1

Serial No.: To be assigned

Art Unit: To be assigned

Filed: Herewith

Examiner: To be assigned

For: METHOD AND SYSTEM

FOR LOGIC VERIFICATION

USING MIRROR

INTERFACE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

For insuring compliance with the applicant's duty of disclosure under 37 CFR § 1.56, the undersigned hereby submits the documents listed on the attached Form PTO-1449 for consideration by the Examiner in charge of the above-identified patent application.

The listing of these materials is not to be construed as an admission that such is actually prior art with respect to the invention of the present invention and is not to be construed that such is material with respect to the present invention.

These documents are being submitted within three months of the filing of the above U.S. national application or of the date of entry of the U.S. national stage in an International Patent Application, therefore, no fee is due.

The Director is hereby authorized to charge payment of any deficiency in the above fee(s) or to charge any additional fees required under 37 CFR § 1.16 or 1.17 or credit any overpayment to Deposit Account No. 22-0185. A duplicate copy of this authorization is attached for the Finance Branch.

Respectfully submitted,

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Date: December 28, 2000

In a par